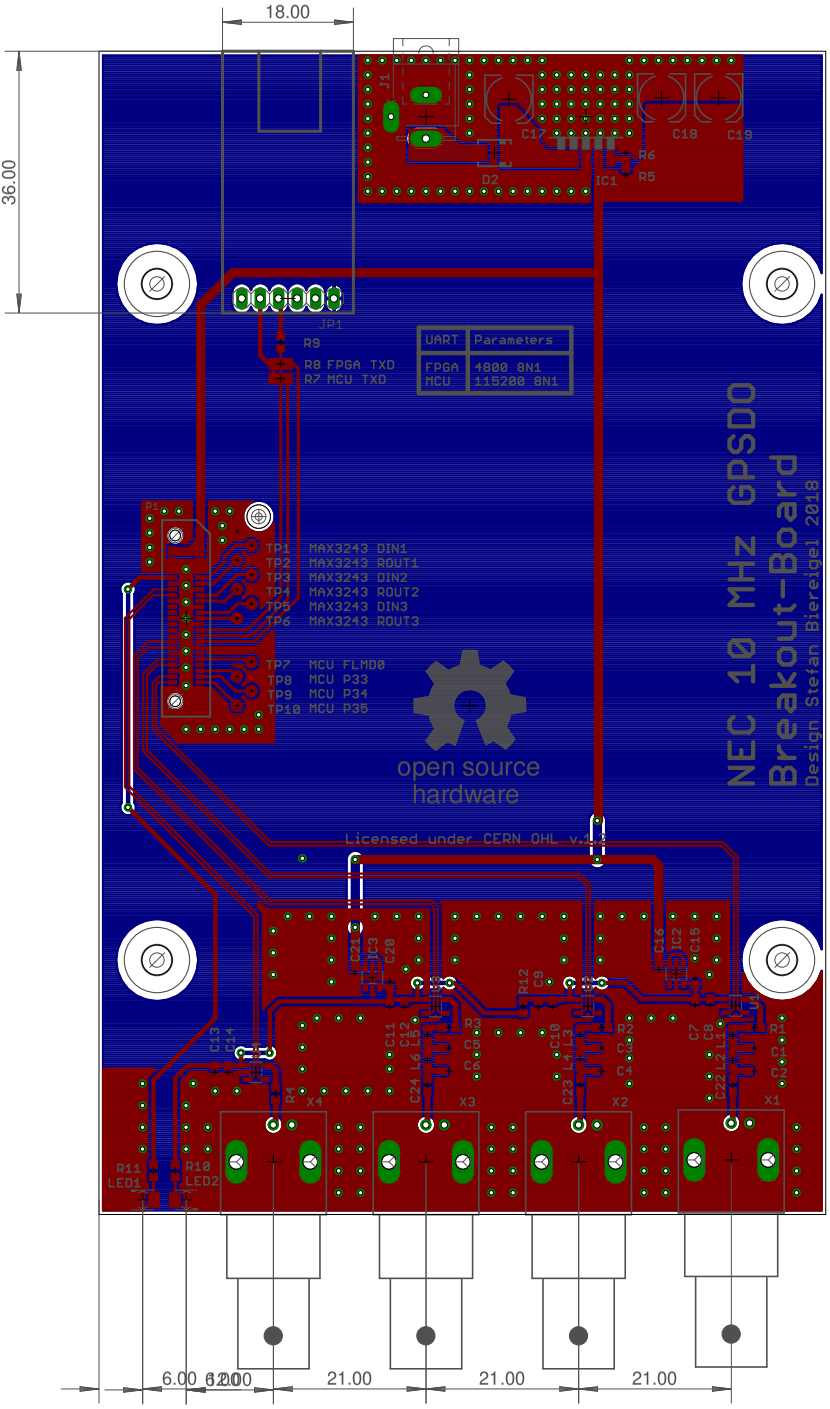
18.00
36.00
NEC 10 MHz GPSDO
Breakout-Board
Design Stefan Biereigel 2018
UART
Parameters
FPGA
4800 8N1
MCU
115200 8N1
R8 FPGA TXD
R7 MCU TXD
TP1 MAX3243 DIN1
TP2 MAX3243 ROUT1
TP3 MAX3243 DIN2
TP4 MAX3243 ROUT2
TP5 MAX3243 DIN3
TP6 MAX3243 ROUT3
TP7 MCU FLMD0
TP8 MCU P33
TP9 MCU P34
TP10 MCU P35
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